

# SY205201SSC Common Mode Filter with ESD Protection

## **General Description**

The SY205201SSC is a highly integrated common mode filter (CMF) providing both electrostatic discharge (ESD) protection and electromagnetic interference (EMI) common mode noise filtering. It can be used for systems using high-speed differential serial interfaces, such as MIPI D-PHY or HDMI.

SY205201SSC can protect and filter two differential line pairs. The SY205201SSC is available in a small RoHScompliant WDFN10 package.

#### **Features**

- Large Differential Bandwidth > 2.5 GHz
- High Common-Mode Stop Band Attenuation:
  - > 20 dB at 1 GHz
  - ±15kV ESD Protection Per Channel (IEC 61000-4-2 level 4, Contact Discharge)
- Low channel input capacitance ensures superior impedance-matching performance.
- WDFN10 2.50mm×2.00mm×0.75mm Package with 0.50mm Lead Pitch
- RoHS-Compliant, Lead-Free Packaging

## Applications

- HDMI/DVI Display in Mobile Phones
- MIPI D-PHY (CSI-2, DSI, etc.) in Mobile Phones and Digital Cameras

### **Mechanical Characteristics**

- DFN 2.5x2.0-10 Package
- Marking: Device Code, Date Code
- Packaging: Tape and Reel



#### Figure 1. Common Mode Filter with Embedded ESD Protection

## **Circuit Diagram**



## **Ordering Information**

Part Number	Package Type	Top Mark
SY205201SSC	DFN2.5×2.0-10 RoHS Compliant and Halogen Free	42YWA

## Pinout (Top View)



# **Marking Codes**



**Note 1:** "42" is device code, fixed. **Note 2:** "YWA" is date code.

# **Pin Description**

Pin Name	Pin No.	Pin Description
ln_1+	1	CMF Channel 1+ to Connector.
In_1-	2	CMF Channel 1- to Connector.
Out_1+	10	CMF Channel 1+ to ASIC.
Out_1-	9	CMF Channel 1- to ASIC.
ln_2+	4	CMF Channel 2+ to Connector.
ln_2-	5	CMF Channel 2- to Connector.
Out_2+	7	CMF Channel 2+ to ASIC.
Out_2-	6	CMF Channel 2- to ASIC.
GND	3, 8	Ground.

Absolute Maximum Rating							
Parameter	Symbol	Min	Max	Unit			
DC Current per Line	IDC		100	mA			
DC Package Power Rating	P <sub>DC</sub>		0.5	Watts			
Human Body Model, MIL-STD-883, Method 3015 Contact Discharge per IEC 61000-4-2 Level 4 <sup>1,2,3</sup>	Vesd	-30 -15	30 15	kV			
Operating Temperature	Торт	-40	+85	°C			
Storage Temperature	T <sub>STG</sub>	-65	+150	°C			



Electrical Characteristics T <sub>A</sub> = 25°C								
Parameter	Symbol	Test Condition	Min	Тур	Мах	Unit		
Channel Resistance	R			3.5	5.0	Ω		
Total Channel Capacitance	CTOTAL	At 1.65VDC Reverse Bias; 1MHz, 30mVAC		0.8	1.3	рF		
Nominal Reverse Working Voltage	Vrwm				5.0	V		
Forward Voltage @I <sub>F</sub>	VF	I <sub>F</sub> = 1mA	0.4		1.2	V		
Breakdown voltage	VBR	I⊤=1mA	6	8	10	V		
Reverse Leakage Current @ VRWM	I <sub>R</sub>	$V_{RWM} = 5V, T_A = 25^{\circ}C$		0.01	0.1	μA		
Dynamic Resistance		Positive Negative		1.36 0.6		ΩΩ		
Differential mode Cut-off Frequency at -3dB Attenuation	fc <sup>3</sup>	$Z_{SOURCE}$ =50 $\Omega$ , $Z_{LOAD}$ =50 $\Omega$		4.0		GHz		
Common Mode Stop Band Attenuation	Fatten	@ 900MHz		16		dB		
Common Mode Impedance	Zc	@ 100MHz		32		Ω		

Note 1: ESD zapping at I/O pins with respect to GND.

Note 2: Un-zapped pins are floating.

Note 3: Specs are verified by measurements.



Figure 2. Uni-Directional TVS



# **Typical Characteristics**

Typical Filter Characteristics (T<sub>A</sub> = 25°C, DC Bias = 0V, 50Ω Environment)



**Note 1:** The clamping voltage was captured at the internal pin while the ESD pulse struck at the corresponding external pin.

Note 2: The clamping voltage test should be conducted with a 1GHz bandwidth oscilloscope.



# **Application Information**

#### **MIPI Application:**

Refer to Figure 3 for the implementation of the SY205201SSC in MIPI applications. Utilize a single SY205201SSC to safeguard the D+, D-, and CLK+/- lines.

#### HDMI Application:

Refer to Figure 4 for the implementation of the SY205201SSC in HDMI applications. Utilize two SY205201SSC devices to protect the high-speed TMDS lines and CLK+/- lines. Additionally, use one SY205209ABC to provide protection for the remaining control lines.



Figure 3. SY205201SSC MIPI D-PHY Application Diagram



Figure 4. SY205201SSC HDMI Application Diagram



# **PCB Layout Guidelines**

For optimum ESD protection and circuit performance, the following circuit board guidelines are recommended:

- Place the SY205201SSC as close to the connectors or terminal ports as possible.
- Use a large via to connect the SY205201SSC pin to the ground.
- The SY205201SSC should be placed near the protected line.
- The distance between the SY205201SSC ground pin and the GND reference path should be as short as possible.



# WDFN10 DFN2.5×2.0-10 Package Outline Drawing



TOP VIEW



SIDE VIEW



DETAIL A ALTERNATE TERMINAL CONSTRUCTIONS



DETAIL B ALTERNATE CONSTRUCTIONS



Package	WDFN10 DFN2.5×1.0-10							
Dim		Millimeters		Inches				
Dim.	Min	Тур	Max	Min	Тур	Max		
Α	0.70	0.75	0.80	0.028	0.030	0.031		
A1	0.00		0.05	0.000		0.002		
A3		0.20 REF		0.008 REF				
b	0.15	0.20	0.25	0.006	0.083	0.010		
D	2.45	2.50	2.55	0.096	0.098	0.100		
E	1.95	2.00	2.05	0.077	0.079	0.081		
е		0.50 BSC			0.020 BSC	2		
L	0.70	0.80	0.90	0.028	0.031	0.035		
L1	0.05		0.15	0.002		0.006		



# **Recommended Mounting Footprint**



Note: All dimensions are in millimeters and exclude mold flash and metal burr.



## **Tape and Reel Specification**

#### **Taping Orientation**



Symbol	W	A0	B0	K0	Е	F	Р	P0	P2	Т
Dimensions	8.00+0.30	2.19	2.77	1.05	1.75	3.50	4.00	4.00	2.00	0.25
(mm)	-0.10	±0.05	±0.05	±0.05	±0.10	±0.05	±0.10	±0.10	±0.05	±0.02

#### **Carrier Tape & Reel Specification for Packages**



Note: All dimensions are in millimeters.

Package Types	Reel Size (Inch)	Qty per Reel(pcs)
WDFN10 DFN2.5×2.0-10	7"	3000



**Revision History** The revision history provided is for informational purposes only and is believed to be accurate; however, it is not warrantied. Please make sure that you have the latest revision.

Date	Revision	Change
04/01/2014	Revision 0.9	Initial Release
04/01/2015	Revision 1.0	Production Release



#### **IMPORTANT NOTICE**

1. **Right to make changes.** Silergy and its subsidiaries (hereafter Silergy) reserve the right to change any information published in this document, including but not limited to circuitry, specification and/or product design, manufacturing or descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products are sold subject to Silergy's standard terms and conditions of sale.

2. Applications. Application examples that are described herein for any of these products are for illustrative purposes only. Silergy makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Buyers are responsible for the design and operation of their applications and products using Silergy products. Silergy or its subsidiaries assume no liability for any application assistance or designs of customer products. It is customer's sole responsibility to determine whether the Silergy product is suitable and fit for the customer's applications and products planned. To minimize the risks associated with customer's products and applications, customer should provide adequate design and operating safeguards. Customer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Silergy assumes no liability related to any default, damage, costs or problem in the customer's applications or products, or the application or use by customer's third-party buyers. Customer will fully indemnify Silergy, its subsidiaries, and their representatives against any damages arising out of the use of any Silergy components in safety-critical applications. It is also buyers' sole responsibility to warrant and guarantee that any intellectual property rights of a third party are not infringed upon when integrating Silergy products into any application. Silergy assumes no responsibility for any said applications or for any use of any circuitry other than circuitry entirely embodied in a Silergy product.

3. Limited warranty and liability. Information furnished by Silergy in this document is believed to be accurate and reliable. However, Silergy makes no representation or warranty, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. In no event shall Silergy be liable for any indirect, incidental, punitive, special or consequential damages, including but not limited to lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges, whether or not such damages are based on tort or negligence, warranty, breach of contract or any other legal theory. Notwithstanding any damages that customer might incur for any reason whatsoever, Silergy' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Standard Terms and Conditions of Sale of Silergy.

4. **Suitability for use.** Customer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of Silergy components in its applications, notwithstanding any applications-related information or support that may be provided by Silergy. Silergy products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Silergy product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Silergy assumes no liability for inclusion and/or use of Silergy products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

5. **Terms and conditions of commercial sale**. Silergy products are sold subject to the standard terms and conditions of commercial sale, as published at http://www.silergy.com/stdterms, unless otherwise agreed in a valid written individual agreement specifically agreed to in writing by an authorized officer of Silergy. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Silergy hereby expressly objects to and denies the application of any customer's general terms and conditions with regard to the purchase of Silergy products by the customer.

6. **No offer to sell or license**. Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights. Silergy makes no representation or warranty that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right. Information published by Silergy regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from Silergy under the patents or other intellectual property of Silergy.

For more information, please visit: www.silergy.com

© 2023 Silergy Corp.

All Rights Reserved.