





March 21, 2018





## **Profile**



- Company Name : Silergy Corp.
- Founded in 2008
- Chairman & CEO: Wei Chen
- Headcounts: 648 (as of February 28, 2018)
- Product : Analog IC
- Shares: 87.73M (as of December 31, 2017)







Silergy Incorporated		ISO9001 Certificatio Silergy Har	ngzhou	Redl Top	O at Taiwan herring Gob o 100 Award	al		2016	2017 Industry first inductorless high efficiency Triac
Silergy US subsidiary Incorporated  Silergy - Hangzhou subsidiary Incorporated  2008 In Fig. 66 Z IO IN Fig. 67 Z IO IN Fig. 6	ndustry First 5V,2A,1MH 2 Sync Buck C in SOT23 Industry First 6V,3.5A,1M Hz Sync Buck IC in DFN3x3	Industry First	ouse of in  2011  World's smallest 6V/6A/1M  Hz Sync Buck in DFN2x2  Industry First	PFC/PSR/QRC Isolated LED Lighting Driver with integrated MOSFET in SO8 (2012 China Innovation Award by Ministry of Industry and	input Smart	Triac Dimmable MR16 LED driver  Industry First DFN4x4 30V/15A Synchronous Boost Regulator	Industry First High Efficiency MOS Integrated	Industry Smallest PFC Triac Dimmable LED Driver with integrated 500V MOSFET in SOT23 package Industry First Dual Output DCDC Regulator in SOT23 package	Dimmable LED Drive  Industry smallest 28V/10A 4-switch Monolithic buckboost converter IC in QFN4x4  Industry Smallest High Efficiency SSD PMIC in







	Name	Share %
Chairman/CEO/ President	Issac Chen	8.17
Director/CTO	Albert You	4.29
Director	施君徽(Ryan Shih)	-
Director	楊光智(David Yang)	-
Independent Director	柯順雄 (Eric Ko)	-
Independent Director	蔡永松 (Daniel Tsai)	-
Independent Director	李鴻基 (Alex Lee)	-
Total		12.46





# **Management Team**

Title	Name Expr		Expertise	Experience		
CEO/ President	Issac Chen, PhD	21	System Architect	PhD, EE, from Virginia Tech Applications Manager, Linear Technology Corp. VP of System and Apps, Monolithic Power System 21 years' industry experience		
СТО	Albert You, PhD	16	Semiconductor Process/Device/ Adv Packaging	PhD, EE, from Virginia Tech Technology Director, Volterra 16 years' industry experience		
VP of Eng.	Michael 32 Grimm		IC Design	MS, EE, from UC Berkeley Managing Director, NB BU, Maxim 32 years' industry experience		
Exec. Design Director	Jaime Tseng	32	IC Design	BS, EE, from UC Berkeley Design Manager, Linear Technology Corp. 32 years' industry experience		



# Silergy Key Points

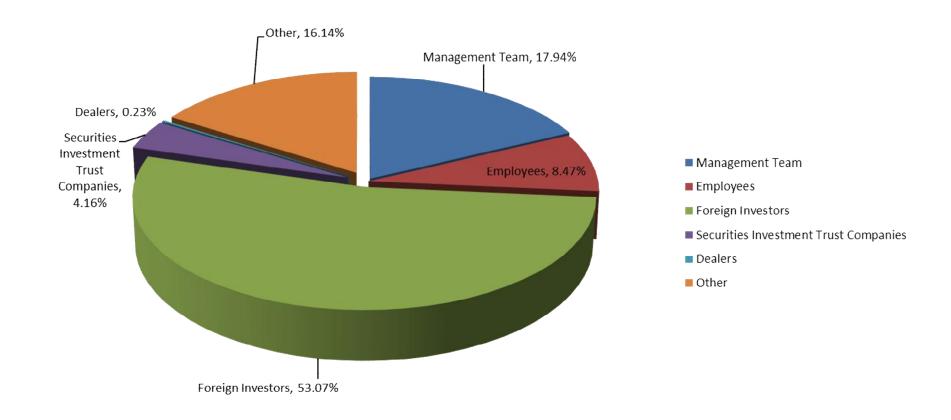


- International Culture: marriage of "West" and "East" culture
  - "West": innovation, leading market positioning
  - "East": execution, diligent, fast response
- Innovation Driven Virtual IDM Platform Model
  - More than 784 patents awarded
  - More than 335 patents pending
- Committed to Developing Industry Leading Products
  - Smaller size
  - Higher efficiency
  - More Intelligence



## **Stable Shareholders Structure**







# **Diversified End Applications**













According ttro acking report aimed at power management IC market made by IHSiSuppli, it predicted moderate growth of market in the future. The total amount will be 38.3 billion dollars in 2017, reaching to 39.9 billion in 2018.

#### The technology trends of power management IC

- •Smaller Size: technology upgrading of portable products
- •Higher Efficiency: longer battery life, meeting government regulation of energy conservation and emission reduction
- •Easy to Use
- •Continuous Cost-Down: higher integration level
- •Development Time Reduction

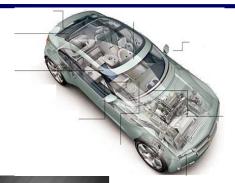


# Future Focus -Smartness/Green Power/ IOT

Silergy+

Inside





Intelligent

**Transportation** 

-JEH.



Wearable

**Device** 





**Robots** 



**Smart Medical** 

**Device** 









# Plan for Product Development



## Aiming at fast-growing consumer and industrial applications



**Payment** 



system









Consumer Products

POS **Mobile** 

**Smart TV** 

**High Res** 

**Wireless** Charger

Goods

White

Wearable device

Industrial **Products** 



ΑI **Application** 



**Automobile Electronics** 



**Smart** Meter



Motor **Drive** 

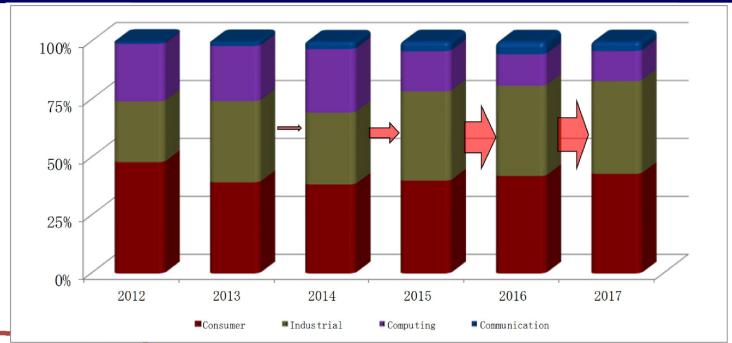


Medical

**Device** 



# Focus on Developing High Margin Business



### Products portfolio

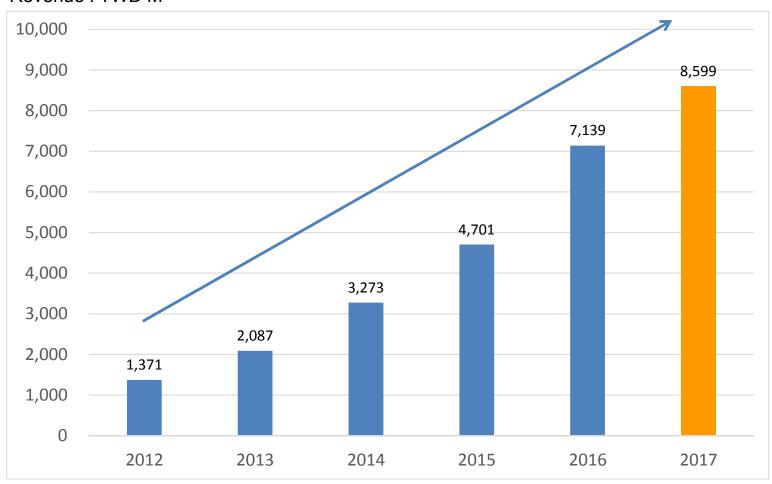
Year	2012	2013	2014	2015	2016	2017
Consumer	48%	39%	38%	41%	42%	43%
Industrial	26%	35%	31%	37%	39%	40%
Computing	25%	24%	27%	17%	14%	13%
Communication	1%	2%	3%	4%	5%	4%



## **Fast Growth**



#### Revenue: TWD M





# **Key Figures-IFRS**



Unit:TWD K

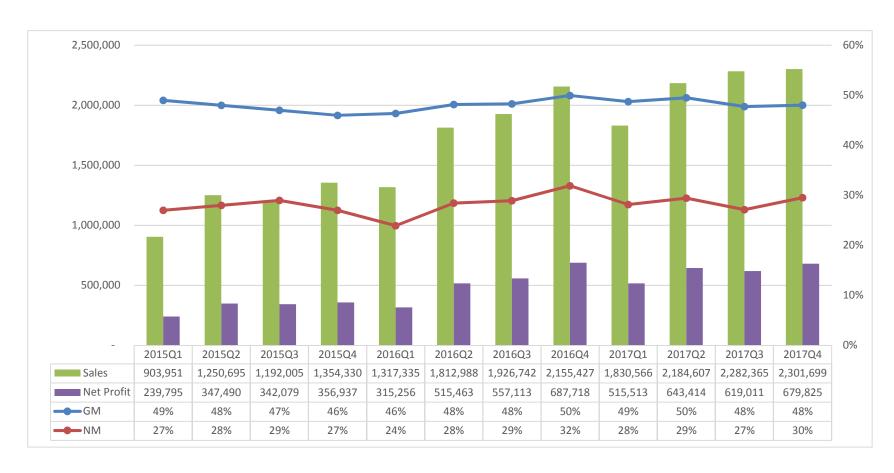




# **Key Figures-Pro Forma**



Unit:TWDK





## Vision



### Misson

Relying on technology innovation to produce analog ICs meeting highest efficiency requirements to reduce energy consumption and to be environmental friendly



### Goal

Best technology leader in analog IC industry



## **Operation Principle**

Upholding the principle of "integrity, creativity, and continuous service", we provide our customers with analog IC with both high efficiency and cost saving, with value added and customized system design support.



# **Our Edge**









# Thanks!