SY22602

Adaptive LED Current Filter For LED Lighting

General Description

The SY22602 is an adaptive linear current regulator that is designed to eliminate low frequency current ripple. It is intended for LED lighting applications, particularly those with single-stage LED drivers. The device acts as a ripple current filter for the LED load. It operates using adaptive control and requires a minimal number of external components.

The part can deliver up to 0.6A output current over a wide output voltage range from 20V to 58V. It uses a proprietary scheme to reduce the power loss and increase the overall efficiency. Multiple devices can operate in parallel to support higher LED current.

The SY22602 provides reliable open/short LED and overtemperature protections.

The SY22602 is available in a compact SO8 package.

Features

- Current Filter for Single-Stage LED Driver to Eliminate Current Ripple
- 20V to 58V Output Voltage Range
- Operating Current: 96 µA (typ.)
- 0.2A to 0.6A Output Current Range
- Proprietary Scheme for Low Power Loss (2.5% or less)
- Open LED and Short LED Protections
- Overtemperature Protection
- RoHS-Compliant and Halogen-Free
- Compact Package: SO8

Applications

LED lighting

Typical Application

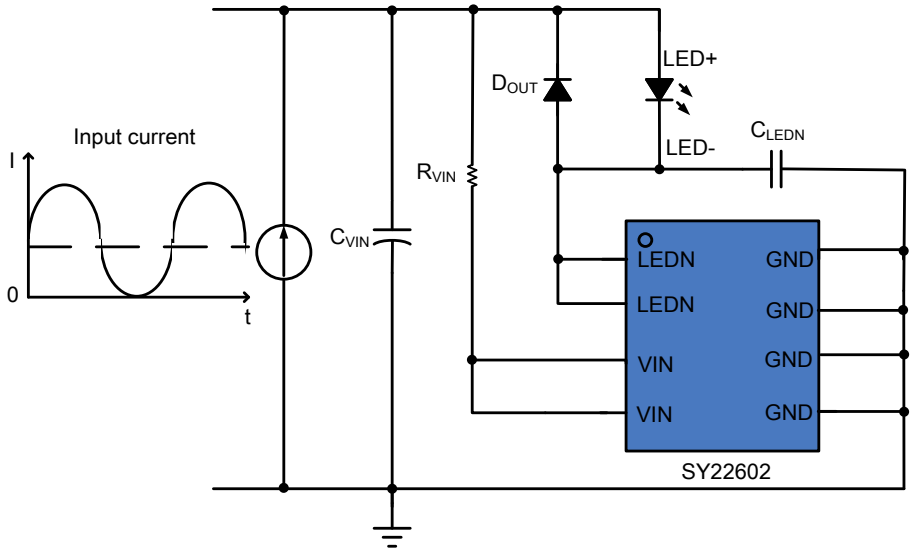


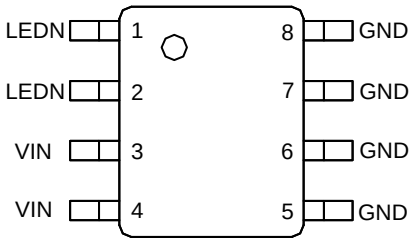
Figure 1. Schematic Diagram

Ordering Information

Ordering Part Number	Package Type	Top Mark
SY22602FAC	SO8 RoHS-Compliant and Halogen-Free	**AYExyz**

x = year code, y = week code, z = lot number code

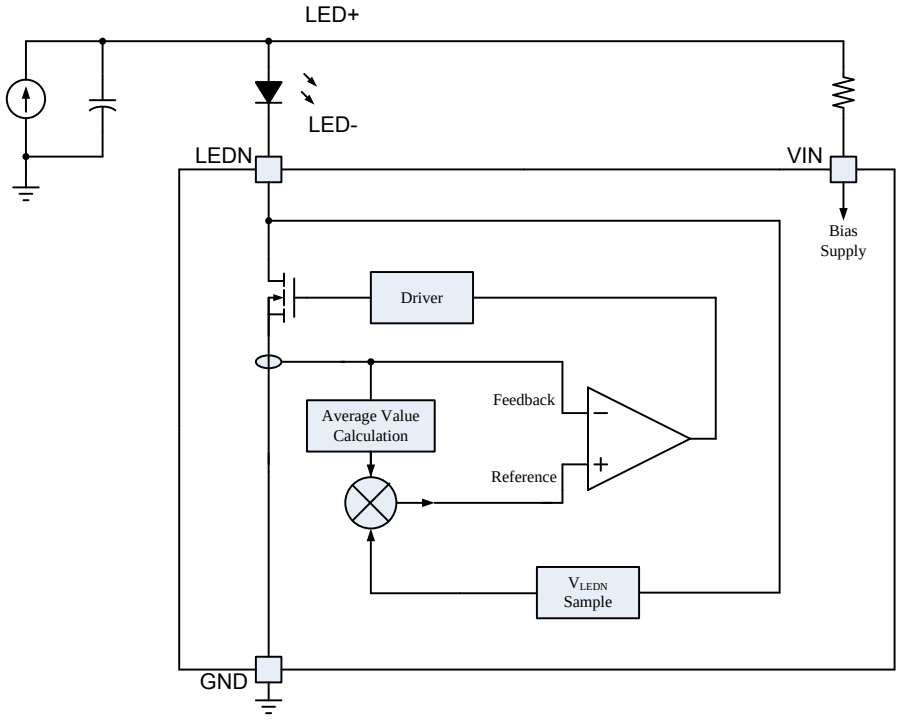
Pinout (top view)



Pin Description

Pin Number	Pin Name	Pin Description
1,2	LEDN	Cathode of LED string
3,4	VIN	Power supply
5–8	GND	Ground pin

Block Diagram



Absolute Maximum Ratings

Parameter (Note 1)	Min	Max	Unit
VIN	-0.3	60	V
LEDN	-0.3	60	
Lead Temperature (Soldering, 10s)		260	°C
Junction Temperature, Operating	-40	150	
Storage Temperature	-65	150	

Thermal Information

Parameter (Note 2)	Typ	Unit
θ_{JA} Junction-to-Ambient Thermal Resistance	88	°C/W
θ_{JC} Junction-to-Case Thermal Resistance	45	
P_D Power Dissipation T_A = 25°C	0.6	W

Recommended Operating Conditions

Parameter	Min	Max	Unit
VIN, LEDN	20	58	V

Electrical Characteristics

(V_{IN} = 2.4V, T_A = 25°C unless otherwise specified.)

Parameter		Symbol	Test Conditions	Typ	Unit
Power Supply	V_{IN} minimum operating voltage	V_{VIN_min}		20	V
	V_{IN} maximum operating voltage	V_{VIN_max}		58	V
	V_{IN} Turn-On Threshold	V_{VIN_ON}		3.5	V
	V_{IN} Turn-Off Threshold	V_{VIN_OFF}		3.3	V
	V_{IN} Operating Current	I_{VIN}		96	μA
LEDN current	LEDN minimum operating current	I_{LEDN_MIN}		200	mA
	LEDN maximum operating current	I_{LEDN_MAX}		600	mA
LEDN voltage	High Voltage Protection	V_{LEDN_HV}		5	V
	Over voltage Protection	V_{LEDN_OV}		14	V
Thermal	Shutdown Temperature 1	T_{SD1}	V_{LEDN} < 15V	150	˚C
	Shutdown Temperature 2	T_{SD2}	V_{LEDN} > 15V	100	˚C
	Hysteresis Temperature	T_{HYS}		20	˚C

Note 1: Stresses beyond the “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

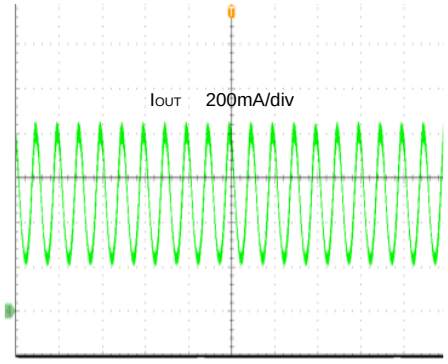
Note 2: θ_{JA} is measured in the natural convection at T_A = 25°C on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard. Test condition: Device mounted on 2” x 2” FR-4 substrate PCB, 2oz copper, with minimum recommended pad on top layer and thermal vias to bottom layer ground plane.

Typical Performance Characteristics

(V_{IN} = 40V, T_A = 25°C unless otherwise specified)

Without SY22602

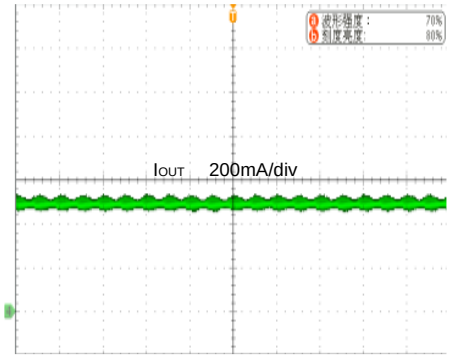
(C_{OUT}=660uF,I_{OUT}=500mA)



Time (20ms/div)

With SY22602

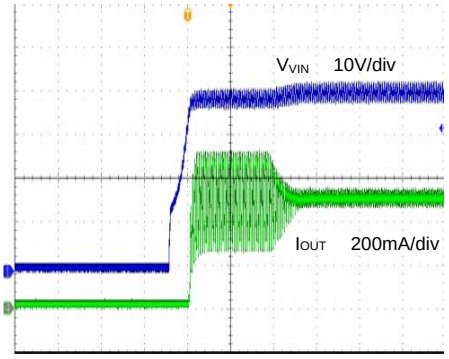
(C_{OUT}=660uF,I_{OUT}=500mA)



Time (20ms/div)

Startup

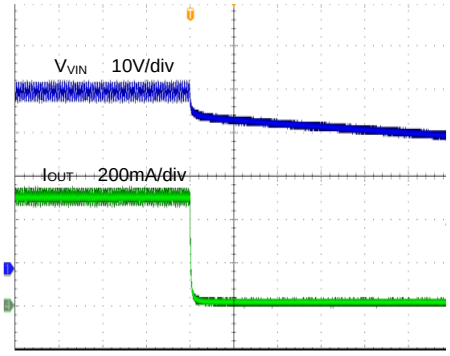
(C_{OUT}=660uF,I_{OUT}=500mA)



Time (200ms/div)

Shutdown

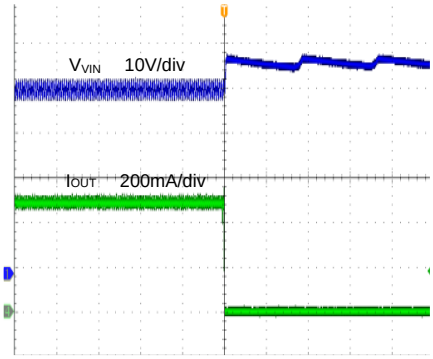
(C_{OUT}=660uF,I_{OUT}=500mA)



Time (200ms/div)

Open LED Protection

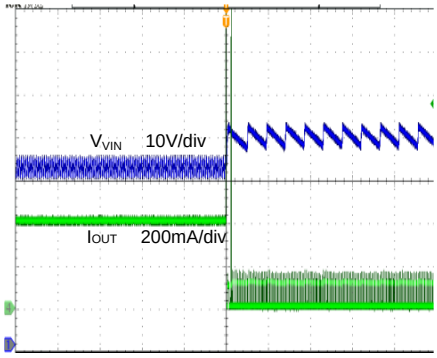
(C_{OUT}=660uF,I_{OUT}=500mA)



Time (200ms/div)

Short LED Protection

(C_{OUT}=660uF,I_{OUT}=500mA)



Time (200ms/div)

General Information

The SY22602 is an adaptive linear current regulator that is designed to eliminate low frequency current ripple going through an external load. It is intended for LED lighting applications, particularly single-stage LED drivers. The device acts as a ripple current filter for the load.

The device can deliver up to 0.6A output current over a wide output voltage range from 20V to 58V. It uses a proprietary scheme to reduce the power loss and increase efficiency. Multiple parts can be operated in parallel for applications requiring higher LED current.

Reliable open/short LED protection (OLP/SLP) and overtemperature protection (OTP) are provided for ensuring reliable operation.

Startup

The SY22602 begins operation when V_{VIN} exceeds V_{VIN_ON}. There is an initial blanking time in which the current filter function is suppressed to allow the internal average current reference to start. After the startup, the LED current ripple is gradually suppressed.

Shutdown

When V_{VIN} falls below V_{VIN_OFF}, the LEDN pin becomes high impedance with respect to the GND reference.

Steady-State Operation

The LED current is sampled and processed inside the device. The average LED current value is taken as the reference to regulate the instantaneous current. V_{LEDN} is sensed simultaneously and kept low to reduce power loss. If the input/output experience transient voltages that causes V_{LEDN} to be higher than V_{LEDN_HV}, the LED current ripple suppression will be reduced to enable V_{LEDN} fall quickly until V_{LEDN} is lower than V_{LEDN_HV}. Therefore, the normal operating voltage for V_{LEDN} will be lower than V_{LEDN_HV}.

Input Capacitor C_{VIN}

The power loss on the chip is related to the input capacitor C_{VIN}. A larger C_{VIN} value reduces the power loss.

The input capacitor C_{VIN} are designed by rules below:

(a) Select an input capacitor C_{VIN}, making sure that the voltage on LEDN pin is lower than V_{LEDN_HV}.

(b) Select C_{VIN} to obtain an optimal power loss P_{LEDN_LOSS} and ensure the power loss and thermal on the chip is acceptable.

$$P_{LEDN\_LOSS} = V_{LEDN\_AVG} * I_{LED}$$

(c) If the C_{VIN} is not large enough to ensure that the power loss and thermal dissipation are acceptable, increase C_{VIN} and go back to step (b) and reiterate until conditions are met.

Note: The recommended input capacitor C_{VIN} value is dependent on the output current and PCB heat dissipation conditions. The relation between the LED current, P_{LOSS_LEDN} and C_{VIN} is shown in figure 2.

I_{LED} (mA)	C_{VIN} (µF)	V_{LEDN_AVG} (V)	P_{LEDN_LOSS}(mW)
250	220	1.92	481
250	330	1.32	331
400	440	1.64	654
400	880	0.91	364
600	880	1.37	820
600	1320	1.01	603

Figure 2

Open LED and Recovery

When a LED is open, LED current and V_{LEDN} both naturally drop to zero, and the current filter function will shut down. When the LED is reconnected, the LED current is sensed and the device will resume normal operation.

Short LED and Recovery

When a LED is shorted, V_{LEDN} is pulled high. If V_{LEDN} is higher than V_{LEDN_OV}, the LED current will be limited and the thermal shutdown threshold is reduced to T_{SD2}.

When the output short is removed, V_{LEDN} is pulled down. If V_{LEDN} is lower than V_{LEDN_OV}, the LED current limit is disabled and the thermal shutdown threshold is changed back to T_{SD1}.

When the LED output is shorted, the external diode D_{OUT} is used to avoid LEDN overshoot, typically caused by the parasitic inductance of the output wiring.

Safety Test

The external D_{OUT}, R_{VIN} and C_{LEDN} are used to protect the VIN/LEDN pins from overvoltage, especially for the ESD and Hi-Pot tests. The recommended values are D_{OUT}= 1N4148 or BAV21W (Fast recovery diode), R_{VIN} = 20KΩ and C_{LEDN} = 100nF.

Parallel Operation Application

Multiple SY22602 devices can operate in parallel to support higher LED current, as shown in Figure 2.

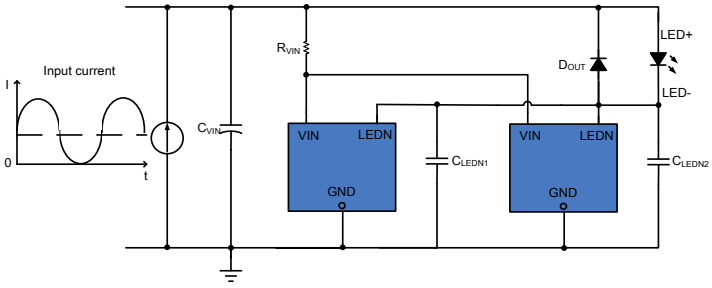


Figure 3. Parallel Circuit

Layout

1) C_{LEDN} must be close to the pins LEDN and GND to protect the LEDN pin from overvoltage.

2) The PCB copper area associated with GND pin and LEDN pin must be maximized to enhance heat dissipation.

3) C_{VIN} must be placed close to IC, D_{OUT} must be placed close to LEDN pin and C_{VIN} to reduce the impact of parasitic inductance on the circuit.

The recommended layout for SY22602 is shown in figure 4

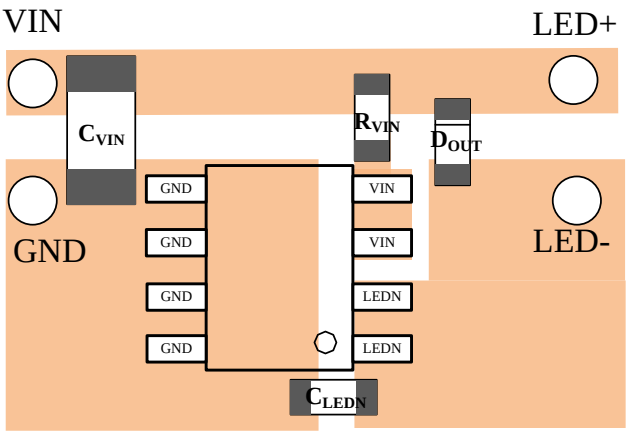
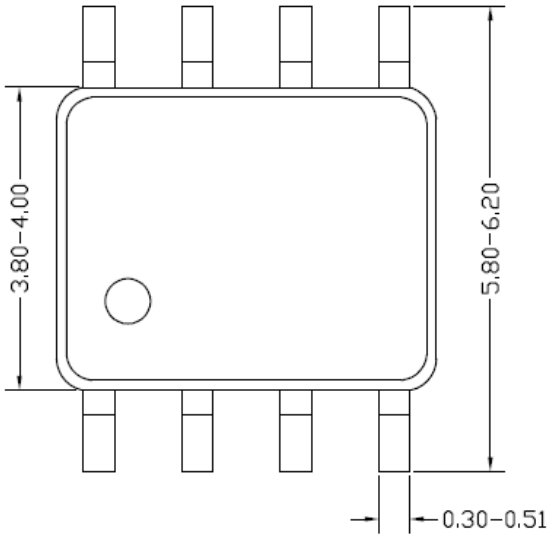
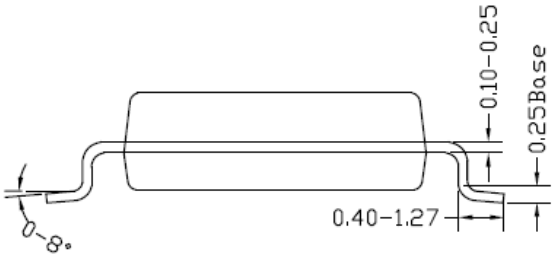


Figure 4. Recommend PCB Layout

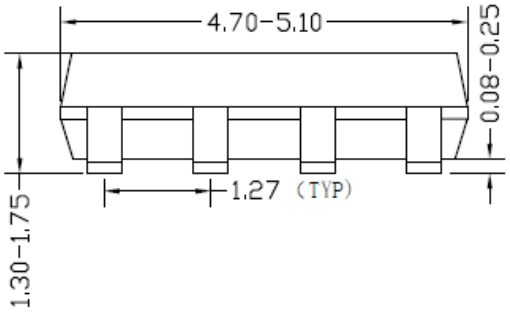
SO8 Package Outline Drawing



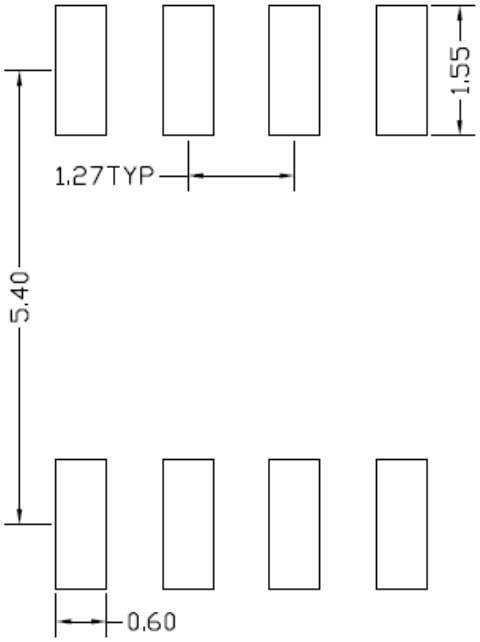
Top view



Side view



Front view



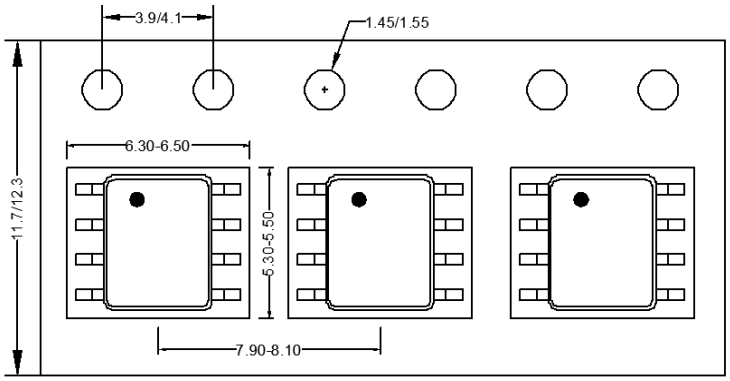
Recommended Pad Layout

(Reference only)

***Notes**: All dimension in millimeter and exclude mold flash & metal burr.*

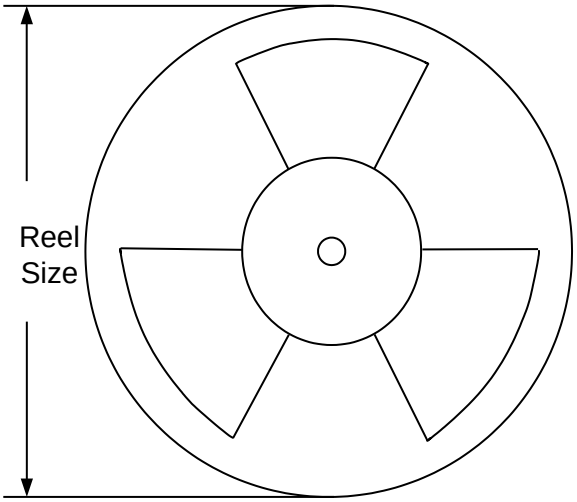
Taping & Reel Specification

SO8 taping orientation



Feeding direction ⟶

Carrier Tape & Reel specification for packages



Package type	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Trailer length(mm)	Leader length (mm)	Qty per reel
SO8	12	8	13"	400	400	2500

Others: NA

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