

General Description

SY205206DWC is a low-capacitance transient voltage suppressor (TVS) designed to provide electrostatic discharge (ESD) protection for high-speed data interfaces. With a typical capacitance of 2pF, SY205206DWC is designed to protect against over-voltage and over-current transient events. It complies with IEC61000-4-2 (ESD) ($\pm 30\text{kV}$ air, $\pm 30\text{kV}$ contact discharge), IEC61000-4-5 (surge) (10A, 8/20 μs).

Each SY205206DWC device can protect one data line. The SY205206DWC is available in a compact DFN1.0x0.6-2 package.

Features

- Transient Protection for High-Speed Data Lines
 - IEC61000-4-2 (ESD) $\pm 30\text{kV}$ (air) $\pm 30\text{kV}$ (contact)
 - IEC61000-4-5 (surge) 10A (8/20 μs)
- For Operating Voltage of 5V and Below
- Protects One Data Line
- Low Capacitance: 2pF
- Low Leakage Current: 0.01 μA @ V_{RWM} (typical)
- Low Clamping Voltage
- Each I/O pin withstands over 1000 ESD strikes for $\pm 8\text{kV}$ contact discharge.

Applications

- USB 2.0
- Portable Electronics
- Desktops, Servers, and Notebooks
- Mobile Phones
- Digital Camera Ports

Mechanical Characteristics

- DFN1.0x0.6-2 Package
- Marking: Device Code, Date Code
- Packaging: Tape and Reel

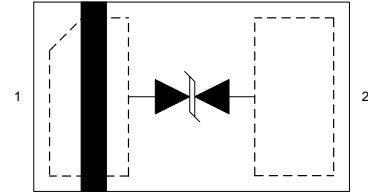
Circuit Diagram



Ordering Information

Part Number	Package Type	Top Mark
SY205206DWC	DFN1.0x0.6-2 RoHS Compliant and Halogen Free	YM

Pinout (Top View)



Marking Codes



Note 1: “Y” is device code, fixed.

Note 2: “M” is date code.

Absolute Maximum Rating				
Parameter	Symbol	Min	Max	Unit
Maximum Peak Pulse Current (8/20μs)	I_{PP}		10	A
Maximum Peak Pulse Power (8/20μs)	P_{PK}		100	W
ESD per IEC 61000-4-2 (Air)	V_{ESD}	-30	30	kV
ESD per IEC 61000-4-2 (Contact)				
Operating Temperature	T_{OPT}	-40	+85	°C
Storage Temperature	T_{STG}	-55	+150	°C

Electrical Characteristics $T_A = 25^\circ\text{C}$						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Nominal Reverse Working Voltage	V_{RWM}		-5		5	V
Reverse Leakage Current @ V_{RWM}	I_R	$V_R = 5\text{V}, T_A = 25^\circ\text{C}$		0.01	0.1	μA
Reverse Breakdown Voltage @ I_T	V_{t1}	$I_T = 1\text{mA}$	5.6			V
Clamping Voltage @ I_{PP}	V_C (1)	$I_{PP} = 16\text{A}, t_p = 10/100\text{ns}$		9		V
Clamping Voltage @ I_{PP}	V_C (1)	$I_{PP} = 10\text{A}, t_p = 8/20\mu\text{s}$		9.5		V
Dynamic Resistance	R_{DYN} (1,2)	$t_p = 10/100\text{ns}$		0.17		Ω
Parasitic Capacitance	C_{ESD} (1)	$V_R = 0\text{V}, f = 1\text{MHz}$		2	3	pF

Note 1: The device is not guaranteed to function outside its operating conditions.

Note 2: R_{DYN} calculated based on $I_{PP}=8\text{A}$ to $I_{PP}=16\text{A}$, $t_p = 10/100\text{ns}$.

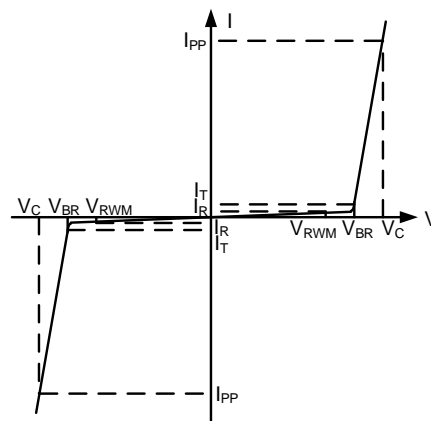
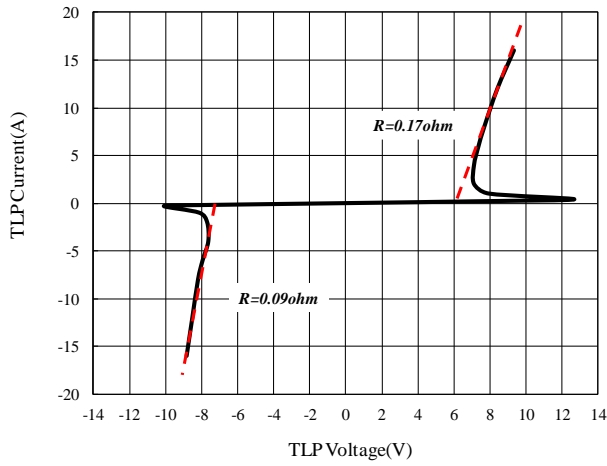


Figure 1. Bi-Directional TVS

Typical Characteristics

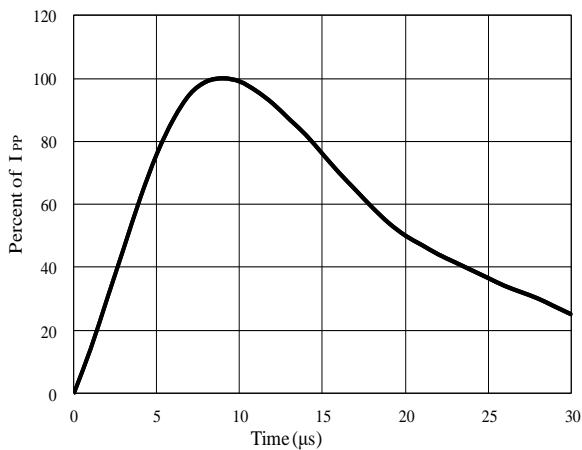
TLP Testing of I/O to I/O



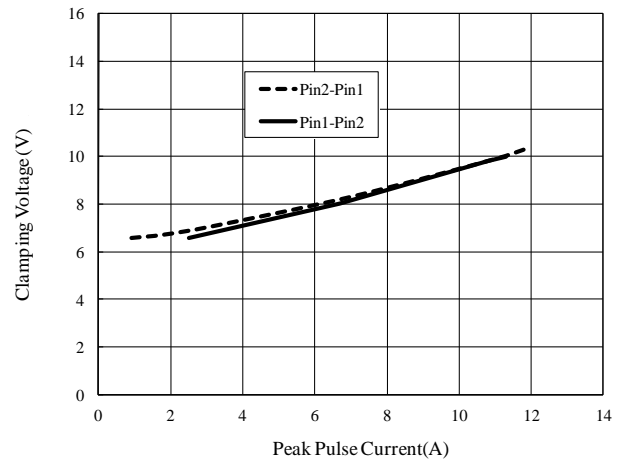
Capacitance vs. Voltage of I/O to I/O



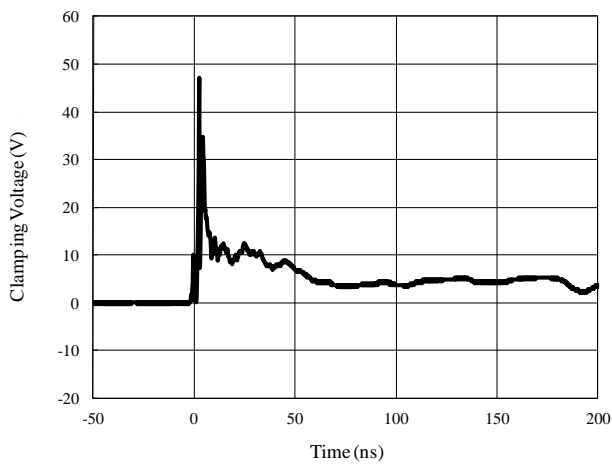
Pulse Waveform



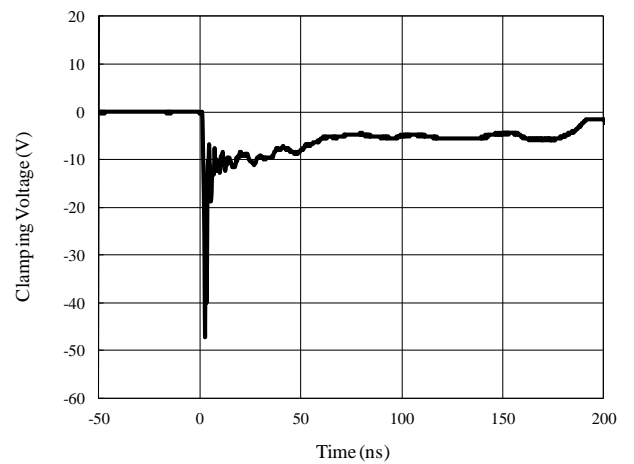
Clamping Voltage vs. Peak Pulse Current (8/20 μs)



ESD Clamping of I/O to I/O (+8kV Contact per IEC 61000-4-2)



ESD Clamping of I/O to I/O (-8kV Contact per IEC 61000-4-2)



Application Information

SY205206DWC is designed to protect one bi-directional data line against over-voltage and over-current transient events by clamping it to an acceptable reference.

The SY205206DWC pin connections are shown in Figure 2. The protected line is connected to Pin1. Pin2 is connected to the GND, which should connect to a ground plane on the board. The connection traces should be as short as possible to minimize the parasitic inductance.

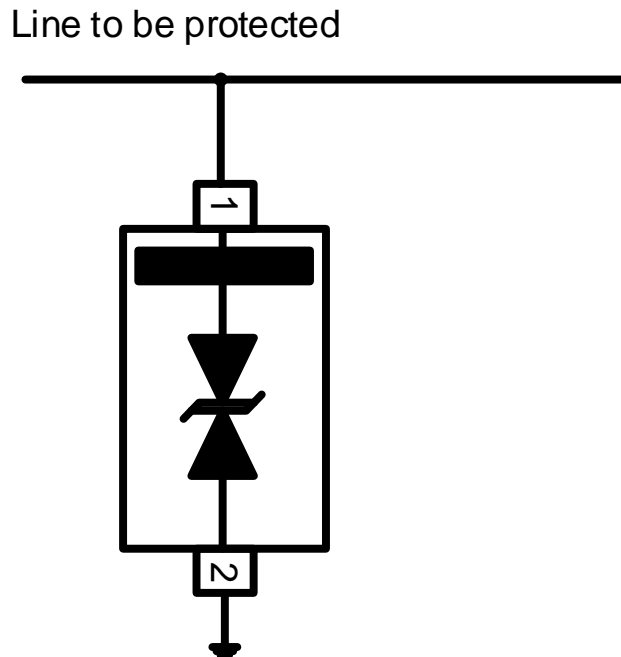


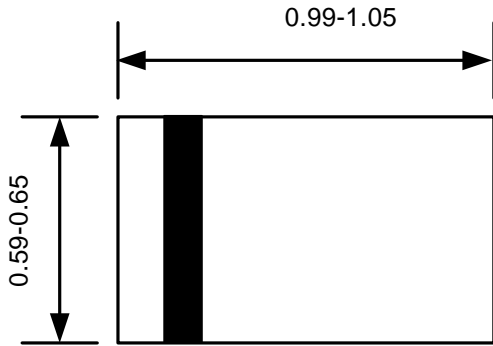
Figure 2. ESD/Surge Protection Circuit

PCB Layout Guidelines

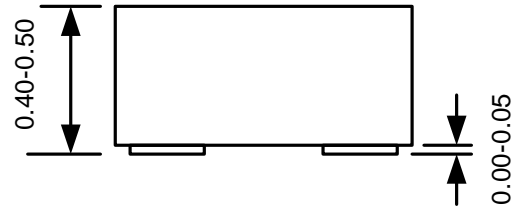
For optimum ESD protection and circuit performance, the following PCB layout guidelines are recommended:

- Place SY205206DWC as close to the connector port as possible.
- Use a large via to connect the SY205206DWC pin to the ground (GND).
- Avoid running signals near board edges.
- The SY205206DWC should be placed near the protected line.
- The distance between SY205206DWC ground pin and the GND reference path should be as short as possible.

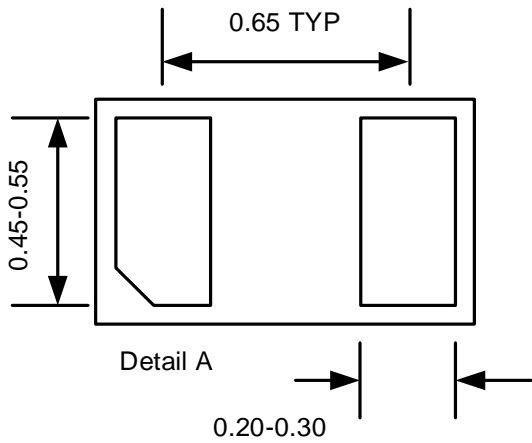
DFN1.0x0.6-2 Package Outline



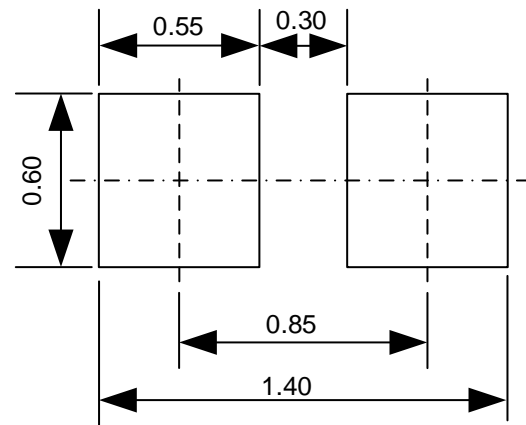
Top View



Side View



Bottom View

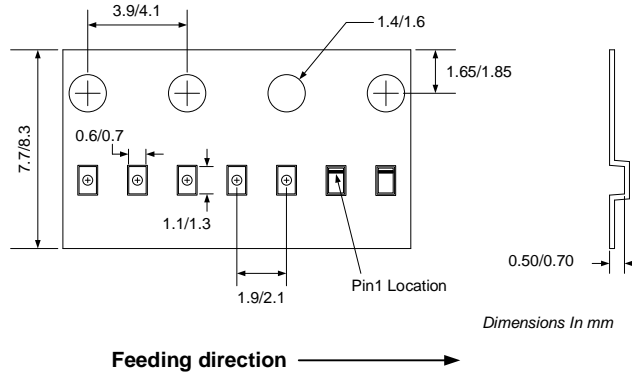


**Recommended PCB Layout
(Reference only)**

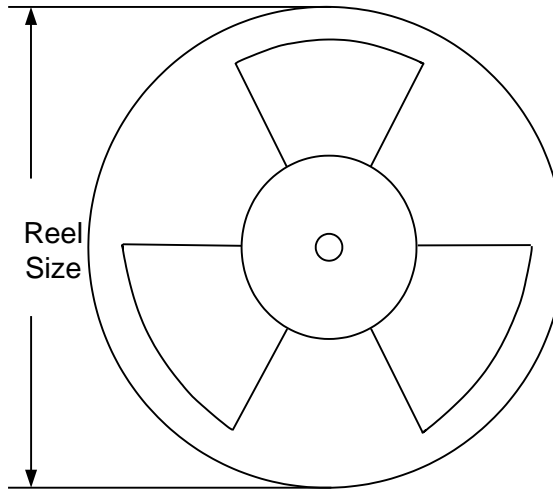
Note: All dimensions are in millimeters and exclude mold flash and metal burr.

Tape and Reel Specification

DFN1.0x0.6-2 Taping Orientation



Carrier Tape & Reel Specification for Packages



Package Types	Tape Width (mm)	Pocket Pitch(mm)	Reel Size (Inch)	Qty per Reel(pcs)
DFN1.0x0.6-2	8	2	7"	10000



Revision History

The revision history provided is for informational purpose only and is believed to be accurate, however, not warranted. Please make sure that you have the latest revision.

Revision Number	Revision Date	Description	Pages changed
0.9	11/20/2020	Initial Release	
1.0	11/20/2021	Production Release	

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